

FIG. 1

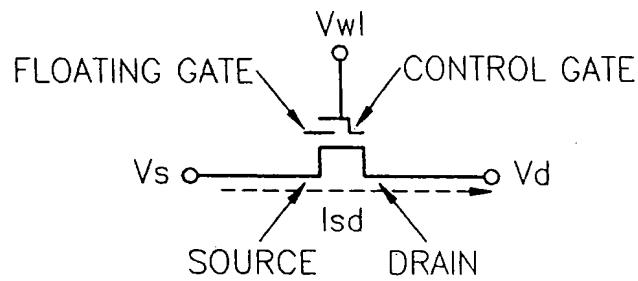


FIG. 2

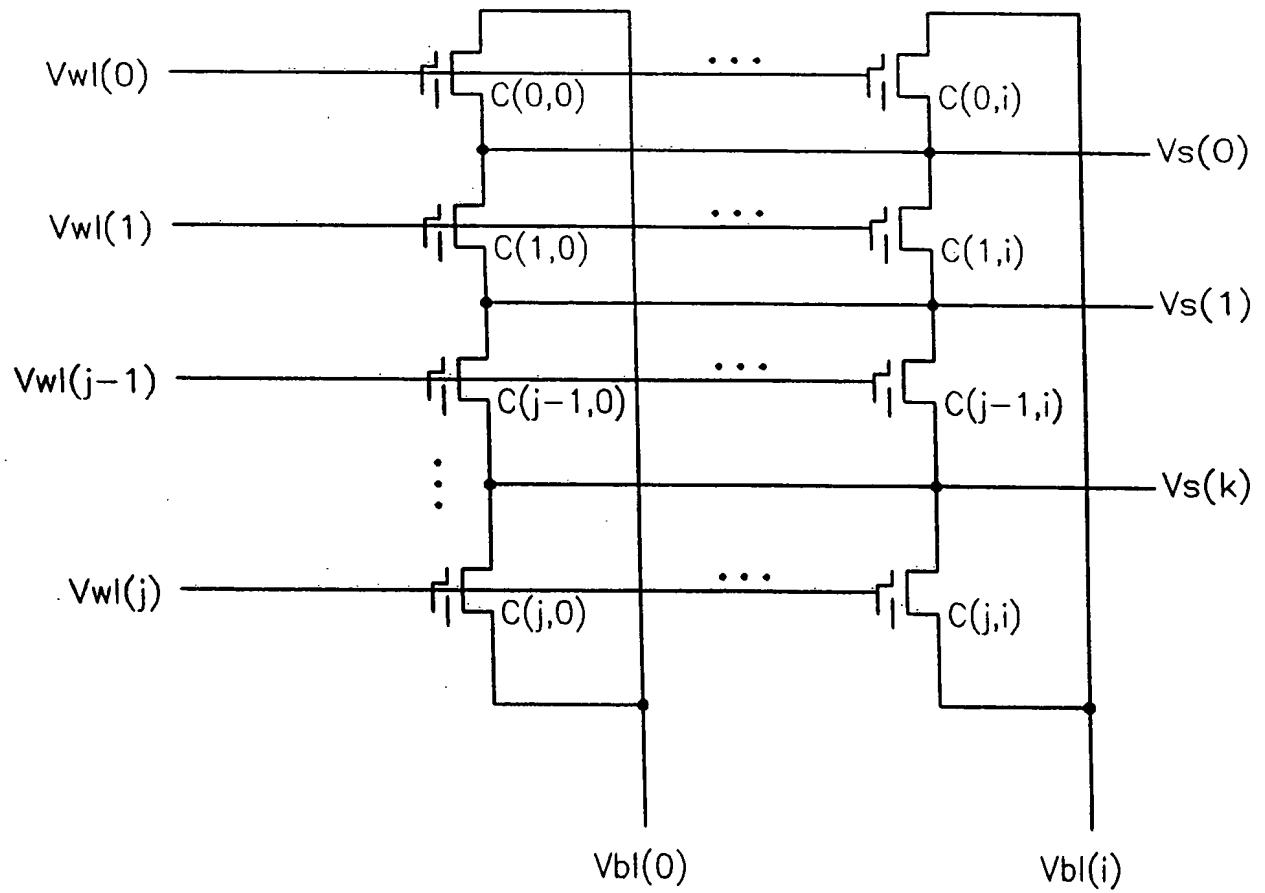


FIG. 3

OPERATION	Vwl [V]	Vs [V]	Vbl [V]
READING	1.5	0	0.7
PROGRAMMING	1.4	8	0.4
ERASURE	11.0	0	0

FIG. 4 (PRIOR ART)

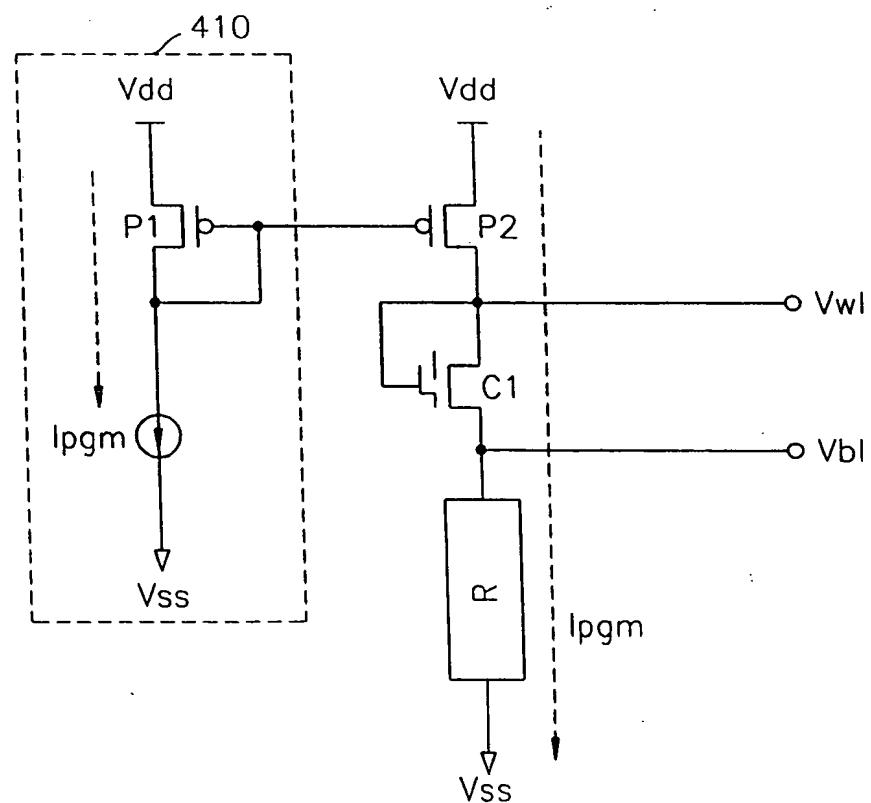


FIG. 5 (PRIOR ART)

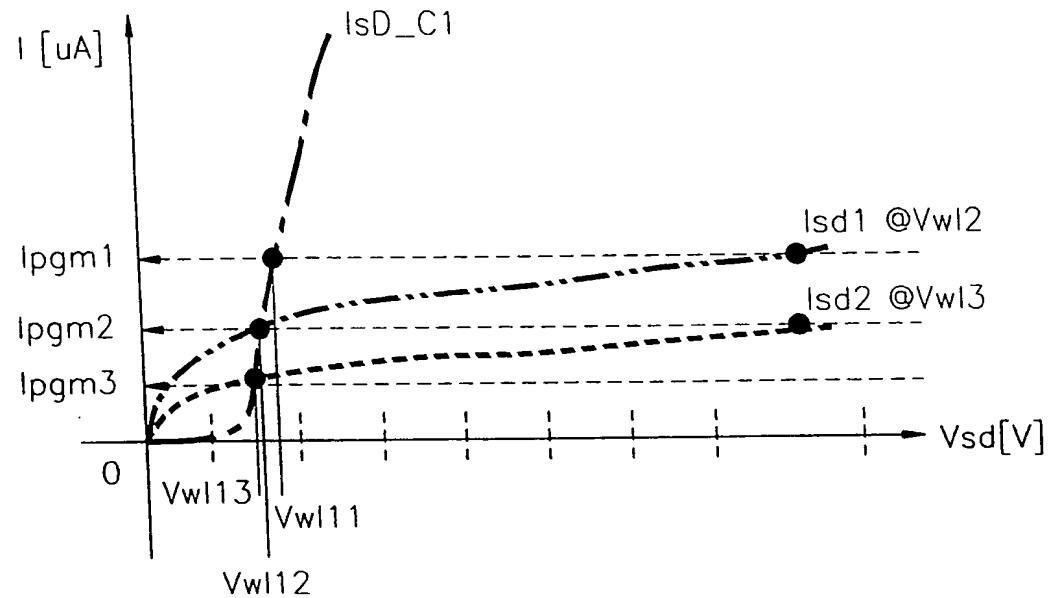


FIG. 6 (PRIOR ART)

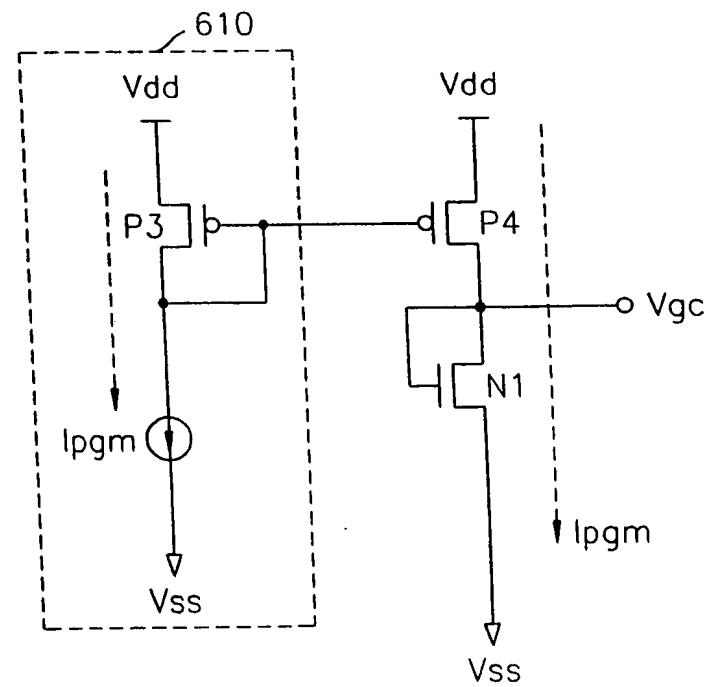


FIG. 7 (PRIOR ART)

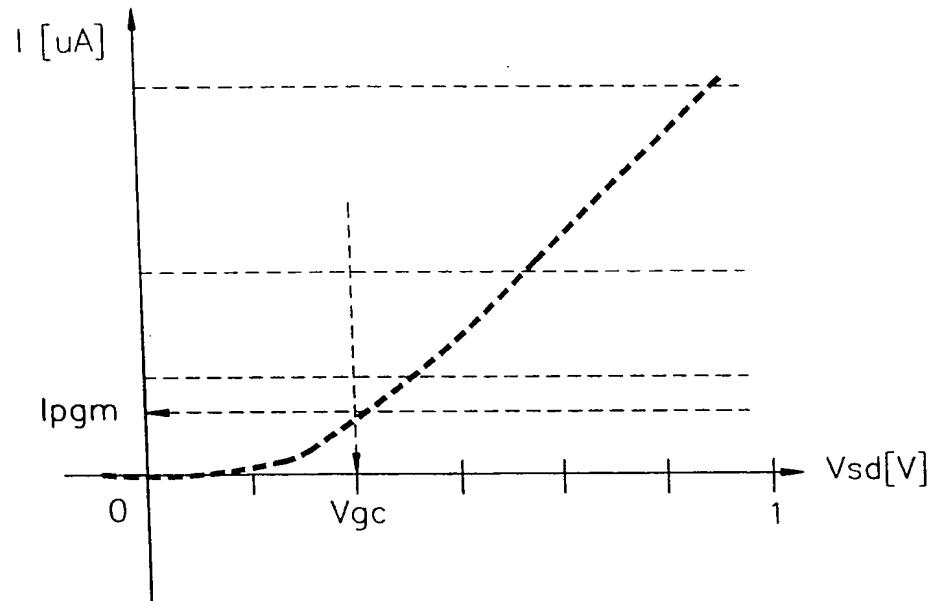


FIG. 8 (PRIOR ART)

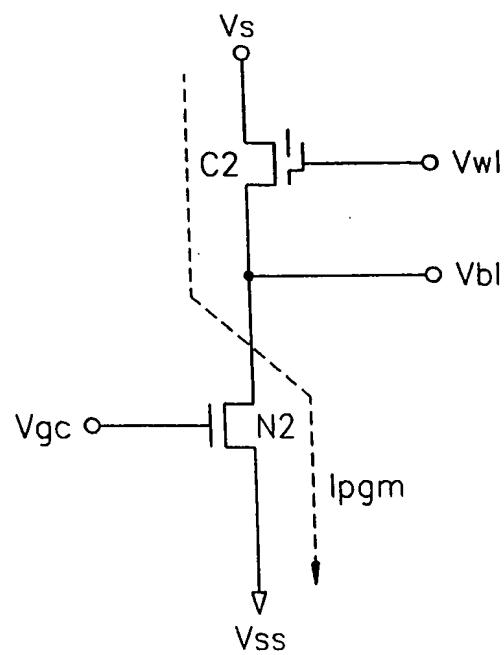


FIG. 9

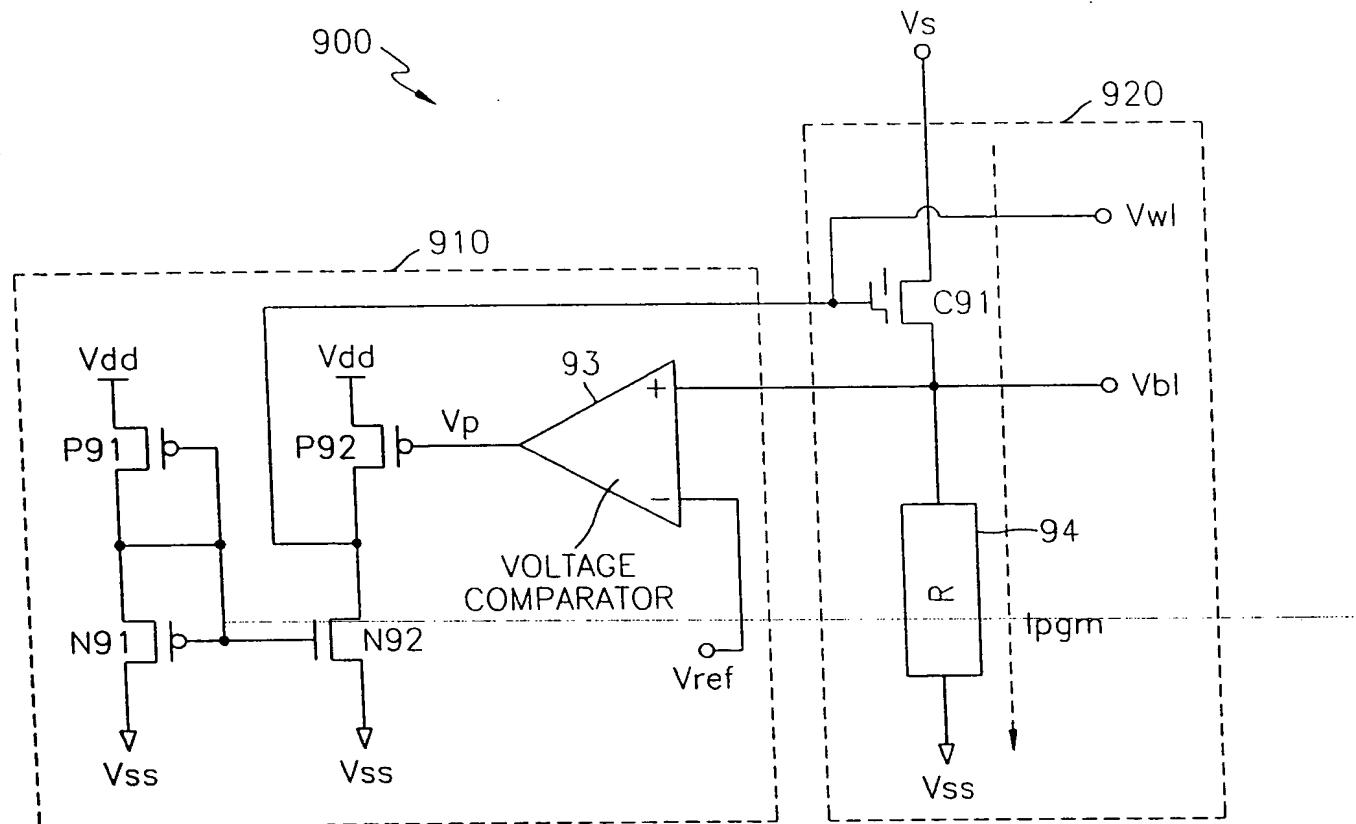


FIG. 10

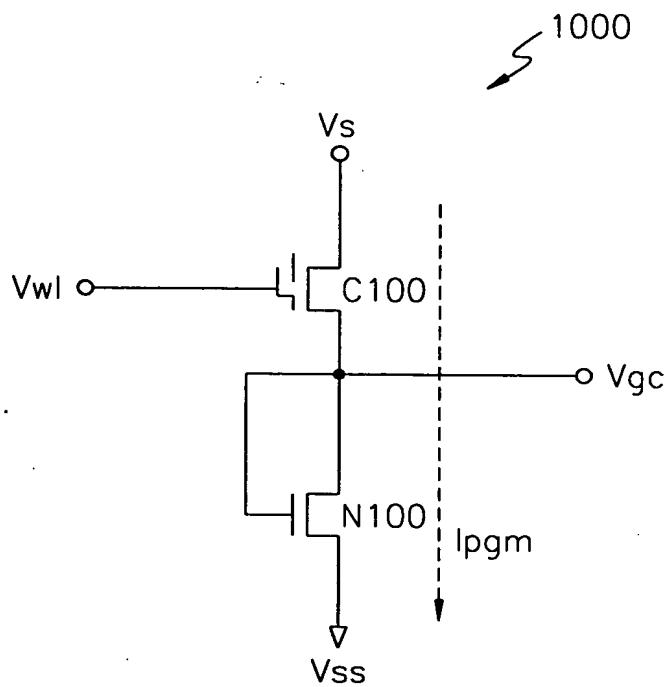


FIG. 11

